

Resistance Transmitter v5 RT243

DESCRIPTION

The RT243 applies a constant current to a resistance device. On change of resistance value the voltage drop across the resistance device will change and becomes the input signal. The RT243 is ideal for in field enclosures or as a space saver in larger control cabinets. Standard output is 4 - 20mA with a minimum supply voltage of 6.3V. This enables the RT243 to be used in 12V battery supply systems or in automotive applications. Other factory set output configurations are 10 - 50mA loop powered and 0 -10mA, 0 - 20mA or voltage output in 3-wire connection up to 40Vdc. Double surge protection is standard with all Series 200 loop powered transmitters to prevent failure due to spikes induced by DC switched inductive loads. The input is isolated from the output as standard. Base calibration is set with 2 metal film resistors on the input card. This input card consists basically of a precision current source adjustable by a 15-turn potentiometer located on the input card. A buffer amplifier is used to condition the input voltage to the uniform card output of 0 - 1V. Final calibration is trimmed using the front



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Block Diagram

accessible zero and span 15-turn trim adjustments. A front mounted L.E.D. and a test socket verify module function and assist in calibration checks without disconnection of output wires.

General Specifications

	Size:	23.5W x 71.5H x 109D (mm).	B	lock Diagram	
I	Mounting:	Clip for 35mm DIN-Rail.		TD	
		ABS.	\cap		
	Housing material:				
	Connection:	Screw terminals.			
	Weight:	0.106 kg.			
	Calibration accuracy:	<0.5%.	°°°		
	Linearity:	<0.5%.	8	o ⁺	
	Protection class:	IP40 (IP65 refer to RT543).			
	Ambient operating range:	-20+70°C.			
	Temperature drift error:	<0.5% within operating range (no	t taking account of	input lead resistance).	
	Supply voltage:	6.3 - 40V continuous (50V 30 sec	onds).		
	Load for 4 20m A output	$RLmax = \frac{SupplyVoltage - 6.3V}{0.02A} \Omega$.			
	Load for 4 - 20mA output:	0.02A 0.02A			
	Load change effect:	0.1% up to RL max.			
	Response time:	0.5 sec to T _{90.}			
I	Optional internal offset adjust:	±50% typical.			
	Front Zero adjust:	+20% / -10% typical.			
I	Front Span adjust:	$\pm 25\%$ typical.			
	Input range:	50Ω up to 10kΩ.			
I	Excitation current:	0.6mA max.			
	Input/output isolation:	>2kV r.m.s.			
I					
	Electromagnetic compatibility:	Complies with AS/NZS 4251.1 (E	N 50081.1) 🛡		
I					
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	For input / output combinations refer to TYPE NO. DESIGNATION overleaf.				
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TYPE NO. DESIGNATION

- Output: -
- 1 = 4 20mA. 2 = 10 - 50mA. *) 3 = 0 - 1mA. *) 4 = 0 - 10mA.
- 2-wire 3 - wire 0V Ref. *) 5 = 0 - 20mA.

Input Span: -

 $1 = 0 - 200 \Omega (100 - 300 \#).$ $2 = 0 - 500 \Omega (250 - 750 \#).$ $3 = 0 - 1k \Omega (500 - 1k5 \#).$ $4 = 0 - 1.5k \Omega (1k - 2k5 \#).$ $5 = 0 - 2.5k \Omega (1k8 - 3k4 \#).$

Action:

1 = Direct.

*) 2 = Reverse.

*) 6 = 0 - 1V.

*) 9 = Other (Specify).

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*) 7 = 0 - 5V min. supply 10.5Vdc.

 $6 = 0 - 3.5k \Omega (2k8 - 4k3 \#).$

 $7 = 0 - 5k \Omega (4k2 - 5k7 \#).$

 $8 = 0 - 10k \Omega (5k - 12k9 \#).$

OUTPUT

TEST

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SPAN

🖉 ZERO

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*) 8 = 0 - 10V min. supply 15.5Vdc.

Options:-

0 = None.

- *) 1 = Output ramp.
- *) 2 = Offset inputs up to 100% of span.
- *) 9 = Other (Specify).

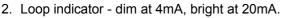
= Indicates span range that can be achieved for the specified input using an internal adjustment. *) = Price Extra.

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Refer to CP24301 for the internal adjustment procedure if required.

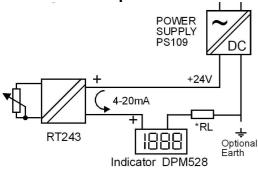
Front Control Explanation

1. Test socket - output signal access with reference to terminal (1) loop integrity is maintained when digital multimeter Rin <30 Ω is used.

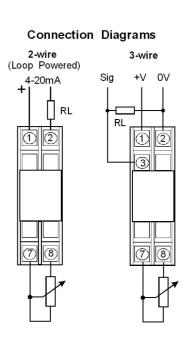


- 3. SPAN (full scale) adjust 15 turn.
- 4. ZERO (start scale) adjust 15 turn.

Connection Example



NOTE: RL is input load of PLC, VSD, or other process instrument.



RT243 - X X X X

3 - wire

0V Ref.

In the interest of development and improvement, APCS reserve the right to amend, without notice, details contained in this publication. APCS will accept no legal liability for any errors, omissions or amendments

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