

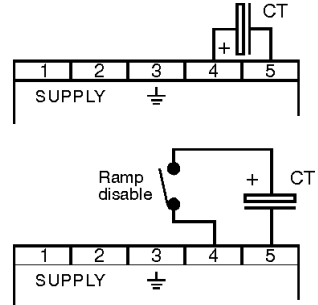
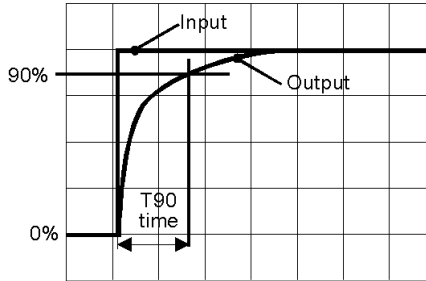
# Output ramp option SI139

## DESCRIPTION

T90 time is defined as follows:

For an input step of 0-100%, the output ramps up to 90% in 't90' seconds (e.g. 18.4ma for 4-20ma input signal).

CT can be an electrolytic capacitor or tag, minimum voltage 16v.



## Capacitor Timing Selection

CT	2.2 $\mu$ F	3.3 $\mu$ F	4.7 $\mu$ F	10 $\mu$ F	22 $\mu$ F	47 $\mu$ F
T90 secs	0.5	0.7	1	2	5	10

$$T_{90} \text{ (Seconds)} = 0.2 \text{ CT}(\mu\text{F})$$

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