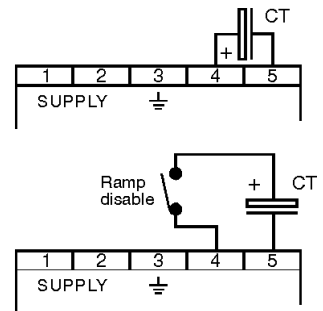
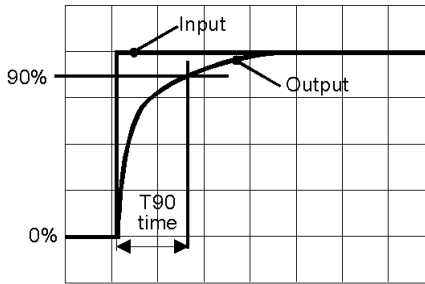


## SI139 Ramp Option

### OPTION 02: RAMP OUTPUT

T<sub>90</sub> time is defined as follows:  
 FOR AN INPUT STEP OF 0-100%,  
 THE OUTPUT RAMPS UP TO 90%  
 IN 'T<sub>90</sub>' SECONDS (e.g. 18.4mA for  
 4-20mA input signal). CT can be an  
 electrolytic capacitor or tag,  
 minimum voltage 16V.



### Capacitor Timing Selection

CT	2.2µF	3.3µF	4.7µF	10µF	22µF	47µF
T <sub>90</sub> secs	0.5	0.7	1	2	5	10

$$T_{90} \text{ (Seconds)} = 0.2 \text{ CT}(\mu\text{F})$$