

OPTION 04: OUTPUT RAMP PD122

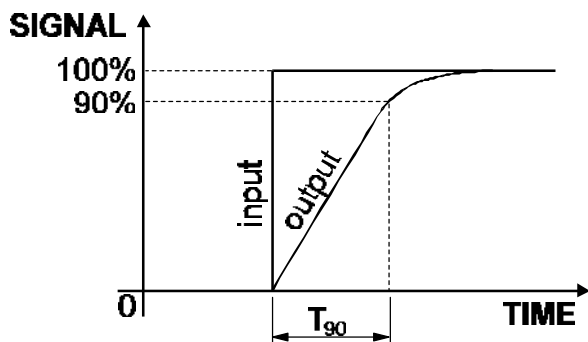
TIMING CAPACITOR SELECTION

CT	T ₉₀
2 μ F	0.5 sec
22 μ F	5 sec
47 μ F	10 sec
100 μ F	20 sec
147 μ F	30 sec

T₉₀ is defined as follows:

For an input step of 0 - 100%, the output ramps up to 90% in T₉₀ seconds (e.g. 18.4mA for 4 - 20mA input signal).

CT can be an electrolytic capacitor or tag, minimum voltage 16V.



CONNECTION DIAGRAM

